

Docket No.: 67161-142

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
Fumitoshi YAMAMOTO, et al. : Confirmation Number:
Serial No.: : Group Art Unit:
Filed: February 27, 2004 : Examiner: Unknown
For: SEMICONDUCTOR DEVICE AND SURGE PROTECTION CIRCUIT

CLAIM OF PRIORITY AND
TRANSMITTAL OF CERTIFIED PRIORITY DOCUMENT

Assistant Commissioner for Patents
Washington, DC 20231
Sir:

In accordance with the provisions of 35 U.S.C. 120, Applicants hereby claim the priority of U. S. Patent Application No. 10/657,106, filed September 9, 2003, of which this application is a Continuation-In-Part, which claims the benefit of the priority under 35 U.S.C. 119 of :

Japanese Patent Application No. 2003-052780(P), filed February 28, 2003
cited in the Declaration of the present application.

The certified copies were filed September 9, 2003 in support of the claim of priority.
Acknowledgement is respectfully requested.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Stephen A. Becker
Registration No. 26,527

600 13th Street, N.W.
Washington, DC 20005-3096
(202) 756-8000 SAB:tlb
Facsimile: (202) 756-8087
Date: February 27, 2004